

Interface, Selection Guide Q2, 2012

INTERFACE PRODUCTS							
Samples	Device	Function	Inputs	Outputs	Max Data-Rate	Package	Features
Transmitters							
Now	TF90LVDS031	Quad LVDS Transmitter	LVC MOS	LVDS	400 Mbps	16-SOIC	
Now	TF90LVDS047	Quad flow-thru LVDS Transmitter	LVC MOS	LVDS	400 Mbps	16-SOIC	Flow-thru Pinout
Receivers							
Now	TF90LVDS032	Quad LVDS Receiver	LVDS	LVC MOS	400 Mbps	16-SOIC	Extended Common Mode
Now	TF90LVDT032	Quad LVDS Receiver	LVDS	LVC MOS	400 Mbps	16-SOIC	Internal termination, ECM
Now	TF90LVDS048	Quad flow-thru LVDS Receiver	LVDS	LVC MOS	400 Mbps	16-SOIC	Extended Common Mode
Now	TF90LVDT048	Quad flow-thru LVDS Receiver	LVDS	LVC MOS	400 Mbps	16-SOIC	Internal termination, ECM
Repeaters							
Now	TF90LVDS104	1 to 4 Repeater	LVDS	LVDS	800 Mbps	16-SOIC	Extended Common Mode
Now	TF90LVDS105	1 to 4 Repeater	LVC MOS	LVDS	400 Mbps	16-SOIC	
Multi-point							
Q2	TF1MC176	M-LVDS Transceiver	LVC MOS/LVDS	LVC MOS/LVDS	125 MHz	8-SOIC	Type 2 Rx
Q2	TF1MD176	M-LVDS Transceiver	LVC MOS/LVDS	LVC MOS/LVDS	125 MHz	8-SOIC	
Q2	TF1MC276	M-LVDS Transceiver	LVC MOS/LVDS	LVC MOS/LVDS	250 MHz	8-SOIC	High-speed, Type 2 Rx
Q2	TF1MD276	M-LVDS Transceiver	LVC MOS/LVDS	LVC MOS/LVDS	250 MHz	8-SOIC	High-speed
Crosspoints							
Now	TF10CP02	2 X 2 Crosspoint	LVDS	LVDS	1.5 Gbps	16-SOIC	Internal Termination
Now	TF10CP22	2 X 2 Crosspoint	LVDS	LVDS	1.5 Gbps	16-SOIC	Low Jitter
System JTAG							
Now	TF112	Addressable 7 port JTAG Mux	IEEE1149.1	IEEE1149.1	25 MHz	100-BGA, 100 TQFP	ATPG support from all major vendors

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